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(54) Information recording system, and recording device and record carrier for use in such an information recording system.

(57) By means of a preformed track modulation (Fig. 1c, Fig. 1d) an auxiliary signal is recorded on a record carrier (1) of an inscribable type in a servo track (4) intended for information recording. The auxiliary signal comprises address codes (AC) and auxiliary codes (HC) which can be distinguished from each other. The address codes specify the address of the track portion in which said codes are recorded, and the auxiliary codes provide additional information about the record carrier, such as references to a track portion having a specific address code.

A recording device comprises means (82, 86, 88, 90, 92) for reading the auxiliary signal recorded by means of the track modulation and means (93) for separately detecting the presence of address codes (AC) and the presence of auxiliary codes (HC) in the auxiliary signal.

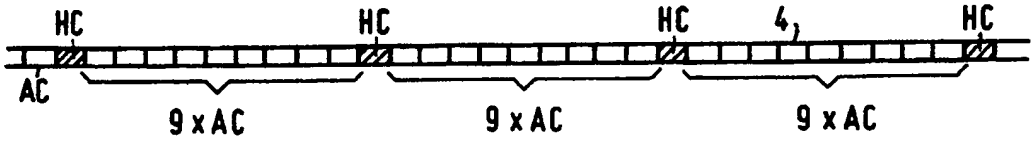

FIG.7

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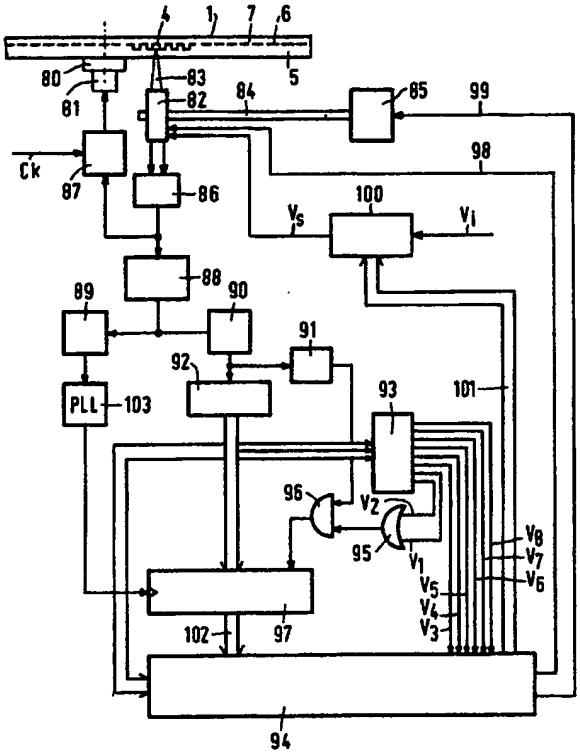
4
1
7
6
5
80
81
83
82
84
85
99
98
Ck
87
86
100
Vs
Vi
88
89
90
91
92
93
101
PLL
103
96
V2
95
V1
V8
V7
V6
V5
V4
V3
102
97
94

FIG.8

excursion of small amplitude has the advantage that the distance between adjacent servo tracks can be small. Fig. 1 also shows the track pitch (the spacing between the track centres) to a substantially larger scale than in reality. In practice the track pitch is approximately 1.6 10^{-6} metres.

An attractive track modulation is that in which the frequency of the track modulation is modulated in conformity with the auxiliary signal. However, other track modulations as described in Netherlands Patent Application NL-A-8701632 (PHN 12.181) are also possible.

Fig. 2 gives an example of a suitable auxiliary signal comprising code signals 12 which alternate with synchronising signals 11. Each code signal 12 may comprise a "biphase-mark" modulated signal having a length of 76 channel bits, which signal is representative of a code word comprising 38 code bits. In the case of a "biphase-mark" modulated signal each code bit is represented by two successive channel bits. A code bit of a first logic value, in the present example "0", is represented by two bits of the same logic value. The other logic value ("1") is represented by two channel bits of different logic values. Moreover, the logic value of the "biphase-mark" modulated signal changes after every pair of channel bits (see Fig. 2), so that the maximum number of successive bits of the same logic value is two at the most. The synchronising signals 11 are selected in such a way that they can be distinguished from the code signals 12. This is achieved when the maximum number of successive bits of the same logic value in the synchronising signals 11 is selected to be three.

Fig. 3 shows a suitable format of 38-bit code words 17 represented by the code signals 12. The code word 17 shown therein comprises three bytes 13, 14 and 15 of 8 bits each and a 14-bit group 16. The most significant bits of the bytes 13, 14 and 15 bear the reference numerals 20, 21 and 22 respectively. The bytes 13, 14 and 15 are used as information bytes and the 14-bit group 16 comprises parity bits for the purpose of error detection. The values represented by the bytes 13, 14 and 15 are referenced mm, ss and ff respectively. Preferably, the 38-bit code words are recorded in the track at equidistant positions and comprise address codes AC and auxiliary codes HC which can be distinguished from eachother and which are recorded in the track, for example in the sequence illustrated in Fig. 7. In Fig .7 a constant number, in the present case 9, of address codes AC is always followed by an auxiliary code HC. However, it is to be noted that the number of address codes AC between the auxiliary codes HC may also be variable instead of constant. The address codes may comprise for example a time code, indicating the time required, when the track 4 is scanned at the nominal scanning speed, to bridge the distance between a reference position in the track and the location where the address code is recorded. Preferably the selected address code is a time code identical to the absolute time code as included in the subcode Q-channel during recording of a CD signal. In that case the value mm specifies a number of minutes and the values ss and ff denote a number of seconds and a number of frames respectively, the specified number of seconds varying between 0 and 59 and the specified number of frames varying between 0 and 74. The number of minutes, seconds and frames may for example BCD encoded in the bytes 13, 14 and 15.

The advantage of the address code described above becomes apparent in particular in the case that a CD signal is to be recorded on the record carrier. In that case the absolute time code to be included in the subcode Q-channel can be derived directly from the address code being read, as is described in detail in Netherlands Patent Application NL-A-880152 (PHN 12.399), herewith incorporated by reference.

When a standard CD signal is recorded three different areas can be distinguished on the record carrier, as is illustrated in Fig. 4, namely:

1) a program area situated between the radial positions bounded by the radii r2 and r3. In this area the data signals are recorded;

2) a lead-in area situated between the radial positions bounded by the radii r1 and r2. This area comprises a lead-in track in which the addresses of the different data signals in the program area are stored in the form of a table of contents, as is described, for example, in Nederlands Patent Application NL-A-8900766 (PHN 12.887), herewith incorporated by reference;

3) a lead-out area situated between the radial positions bounded by the radii r3 and r4. This area comprises a lead-out track in which a lead-out signal which can be distinguished from the data signals is recorded to mark the end of the program area.

The radial positions of the beginning of the lead in area and the beginning of the program area are prescribed by the CD standard, the required distance from the beginning of the lead in area to the centre of rotation r0 being 23 mm, while the distance from the beginning of the program area to the centre of rotation r0 should be 25 mm. Moreover, the lead-out area is required to begin before a predetermined radial position.

In the CD signal the beginning of the program area is indicated by the absolute time code 00:00:00 in the subcode Q-channel. In the program area the absolute time code starts with a value 00:00:00.

For an optimum use of the address code recorded by means of the track modulation it is desirable that

the values of the address codes recorded in the track vary identically to the absolute time code in the CD signal to be recorded. This means that the value of the address code in the track portion whose the radial position is indicated by r2 is 00:00:00. Preferably the value of the address code in the lead-in area increases to a value 99:59:74 at the end of this area. This has the advantage that the value 00:00:00 of the first address code in the program area directly follows the value 99:59:74 of the last address code in the lead-in area. The resulting variation of the values of the address codes is denoted by the reference numeral 50 in Fig. 5.

The value of the address code at the radial position indicated by r1 is referenced AVI and the value of the address code at the radial track position defined by r3 is referenced AVO.

It is to be noted that the values of the address codes at the radial positions defined by the radii r1 and r3 depend strongly on the track pitch and the nominal scanning speed, which may be selected between 1.2 and 1.4 m/s in conformity with the CD standard. Indeed, the total track length between two radial positions is inversely proportional to the track pitch and the normal scanning speed. By way of illustration a line 51 in Fig.5 shows the variation of address code value for a comparatively small track pitch or a low scanning speed. The associated values of the address codes at the radial positions defined by r1 and r3 are referenced AVI′ and AVO′ respectively. As is apparent from Fig. 5 the radial positions defined by r1 and r3 do not correspond to each other for different record carriers on account of the permissible differences in track pitch and scanning speed in accordance with the CD standard. However, for recording CD signals in conformity with the CD standard it is desirable that the radial positions defined by r1 and r3 can be determined accurately. This can be achieved for example by marking the radial positions defined by r1 and r3 with specific auxiliary codes recorded in the track at these radial positions. However, it is to be preferred to record auxiliary codes in the track in the actual lead-in area and/or program area, which auxiliary codes refer to the address codes of the values AVI and AVO which define radial positions indicated by r1 and r3. For a given track pitch and scanning speed the values of AVI and AVO can be pre-calculated and can thus be recorded during recording of the servo track in the mastering process.

This can be achieved simply by including the values of AVI and AVO in the auxiliary codes. The last mentioned possibility is to be preferred because in that case the positions where the auxiliary codes are recorded can be selected freely. Moreover, it is then possible to divide the auxiliary codes containing the values AVI and AVO uniformly along the track, so that the auxiliary codes with the references to AVO and AVI can always be found rapidly.

As already stated, it should be possible for the auxiliary codes and the address codes to be distinguished from one another. This can be achieved, for example, if the code signals representing the address codes and the code signals representing the auxiliary codes are preceded by different synchronisating signals 11. A number of different synchronisation signals 11 which may be used in conjunction with the code signals 12 described herein are described inter alia in Netherlands Patent Application NL-A-8801275 (PHN 12.571).

However, the auxiliary codes can also be distinguished from the address codes if the auxiliary code contains specific bit combinations in which do not occur in the address code. If the time codes described above are used for the address codes, this is possible by means of a bit combination comprising the most significant bits 20, 21 and 22 for the three bytes 12, 14 and 15, which will be explained with reference to Fig. 6.

In Fig. 6 the reference numeral 66 denotes the possible bit combination of the address codes in the lead-in area. On account of the high value mm of the byte 13 in the lead-in area the most significant bit 20 of the byte 13 in this area will always have the logic value 1. The value ss of the byte 14 varies between 0 and 59, which in the case of the BCD encoding means that the most significant bit 21 of the byte 14 always has the logic value 0. The value ff of the byte 15 varies between 0 and 74, so that the most significant bit 22 for the byte 15 also always has the logic value 0. The other bits of the bytes 13, 14 and 15 in the bit combination 66 may assume either the logic value 0 or the logic value 1, which is indicated by the symbol "x".

The reference numeral 67 denotes the possible bit combinations of the bytes 13, 14 and 15 of the address code in the track portion situated outside the lead in area. For the same reasons as in the case of the address code in the lead in area the most significant bits 21 and 22 of the bytes 14 and 15 in the bit combination 67 always have the logic value 0. Moreover, on account of the limited playing time of the record carriers address codes having a value for which the most significant bit 20 in the bit combination 67 assumes the value 1 do not occur in the program area.

The reference numerals 61, 62, 63, 64, 65 and 69 represent a number of bit combinations for which the combination of the most significant bits 20, 21 and 22 of the bytes 13, 14 and 15 differ from the corresponding bit combinations in the address codes. Therefore the bit combinations may be used for the

auxiliary codes HC, in which case the seven least significant bits of the bytes 13, 14 and 15 can represent additional information. For example the bit combinations 61 can be employed to represent the address code of the value AVO. Since the most significant bits of the bytes 13, 14 and 15 of the address codes for AVO always assume the same logic 0 value, the value of AVO may be represented completely by the seven least significant bits of the bytes 13, 14 and 15 in the bit combination 61.

Similarly, the value AVI may be represented by the bit combination 62. The bit combinations 63, 64 and 65 can be employed for auxiliary codes with which other additional information is recorded in the track, such as for example the write energy required for recording, the type of record carrier, the write strategy etc.

Fig.8 shows an embodiment of a recording device for recording data signals Vi on the record carrier 1 described above. The record carrier 1 is placed on a turntable 80, which is rotated by means of a motor 81. An optical write/read head 82 of a customary type is arranged opposite the carrier 1 to read and/or record information from/in the track 4 by means of a radiation beam 83 which is aimed at the recording layer 6. The read head 82 is movable in a radial direction relative to the record carrier 1 by means of a system which comprises, for example, a motor 85 and a spindle 84. The beam 83 is focused on the recording layer 6 in a customary manner and is kept on the track 4 with the aid of customary focusing and tracking systems, not shown. As the track is scanned the beam is modulated in conformity with the preformed track modulation. In the read head 82 the beam which is thus modulated is detected by means of customary radiation-sensitive detectors, which generate signal currents from which a detection signal Vd is derived by means of a detection circuit in a manner as described, for example, in Netherlands Patent Application NL-A-870065 (PHN 12.063), frequency of said detection signal corresponding to the frequency of the track modulation. The detection signal Vd is applied to a motor control circuit 87 to control the speed of the motor in such a way that the frequency of the detection signal Vd remains equal to the frequency of a reference clock signal Ck. The motor control circuit 87 may comprise, for example, a phase detector for detecting the phase difference between the detection signal Vd and the reference clock signal Ck, and a power supply circuit for powering the motor with a voltage whose value depends on the phase difference thus detected.

The detection signal Vd is also applied to an FM demodulator 88 to recover the auxiliary signal Vh from the detection signal Vd. The recovered auxiliary signal Vh is applied to a synchronising-signal detection circuit 89 for detecting the synchronising signals 11 in the auxiliary signal Vh and a "biphase-mark" demodulator 90 for converting the auxiliary signals into the 38-bit code words 17. The bits of the 38-bit code words 17 are serially applied to an error-detection circuit 91, which by means of the parity bits of the bit group 16 determines whether the code word is received without errors. Moreover, the serially supplied code words 17 are applied to serial-parallel converter 92, on whose output the bits of the bytes 13, 14 and 15 are available in parallel form. The most significant bits 20, 21 and 22 of the 3 bytes 13, 14 and 15 applied to the outputs of the converter 92 are fed to a customary decoder circuit 93, which generates eight signals V1, V2, V3, V4, V5, V6, V7, V8, the relationship between the signals V1, ... V8 and the most significant bits 20, 21 and 22 of the bytes 13, 14 and 15 being given in the following Table 1.

TABLE 1.

		MSB	
	20	21	22
V1	1	0	0
V2	0	0	0
V3	0	1	0
V4	1	1	0
V5	0	0	1
V6	0	1	1
V7	1	1	1
V8	1	0	1

The signals V3, V4, V5, V6, V7 and V8 are applied to a control circuit 94 for controlling the recording device. The control circuit 94 may comprise a microcomputer of a customary type, which is loaded with suitable control program to be described hereinafter. The signals V1 and V2 on the output of the decoder

circuit 93 are applied to a two-input OR gate 95. The signals V1 and V2 indicate that the bytes 13, 14 and 15 on the output of the converter 92 represent an address code within the lead-in area or an address code outside the lead-in area, so that the output signal of the OR gate 95 always indicates whether the output signal of the converter 92 concerns address information. The output signal of the OR gate 95 is applied to an input of the two-input AND gate 96. A signal from the error-detection circuit 90, which signal each time indicates by means of a pulse that the received code word 17 is correct, is applied to the other input of the AND gate 96.

Thus, by means of a pulse the AND gate 96 each time indicates whether the information on the output of the converter represents a correctly read address code. The output signal of the AND gate 96 is applied to a parallel load-enable input of a counting circuit 97. The bytes 13, 14 and 15 appearing on the outputs of the converter 92 are applied to the parallel inputs of the counting circuit 97, so that upon every receipt of a correctly read address code the counting circuit 97 is loaded with the address code being read. The counting circuit 97 is of a type which in response to a clock pulse on a clock input of the counter increments the count of the counter by one. A clock signal which is in synchronism with the synchronisation signals 11 being read is applied to said clock input, which clock signal is derived from an output signal of the synchronising signal detection circuit 89 by means of a customary phase locked loop circuit 103.

The counting circuit 97 operates as follows. In response to the clock signal pulses supplied by the phase locked loop circuit 103 the count is each time incremented by one in synchronism with the operation of reading the code values recorded in the track. This means that as soon as the count has a value corresponding to the address codes being read the value of the count will follow the values of the address code being read regardless of whether the subsequent address codes are read incorrectly or the next code word 17 being read does not contain an address code AC but an auxiliary code HC. If at the beginning the count does not correspond to the address code being read the counter is responsive to the next pulse on the output of the AND gate 96 to be loaded with the correct value supplied by the converter 92 via its parallel inputs.

As long as the count correspond to the address code being read the pulses on the output of the AND gate will have no effect because the count then already corresponds to the address code applied to the parallel inputs of the counting circuit. This method of deriving the address codes have the advantage that always an address code corresponding to the position of the track being scanned is available, even in the case that instead of an address code AC an auxiliary code HC is recorded in the track. This also means that the track portions in which the address codes are recorded remain addressable. The bytes 13, 14 and 15 on the output of the converter 92 are applied not only to the parallel inputs of the counting circuit 97 but also the the control circuit 94.

Moreover, the control circuit 94 is coupled to the write/read head 82 and the motor 85 via signal lines 98 and 99. The read head can be set to a read mode or a write mode via the signal line $\overline{98}$. In the read mode the intensity of the radiation beam 83 remains at a constant small value, which is too small to bring about the optically detectable change in the recording layer 6. In the write mode the intensity of the radiation beam is switched in accordance with a write signal Vs between a lower intensity level, which does not bring about an optically detectable change, and a high intensity level (also referred to as write energy), which gives rise to an optically detectable change in the recording layer 6, so that an optically detectable pattern corresponding to the write signal Vs is formed in the recording layer 6. The write signal Vs is generated by an EFM modulation circuit 100, which converts a data signal Vi and the subcode information applied via a bus 101 into an EFM modulated signal in conformity with the CD standard, from which the write signal Vs is derived in a manner as described in detail in, for example,Netherlands Patent Application NL-A-8700934 (PHQ 87.009). Moreover, the high write level (write energy) can be set to a value indicated in the auxiliary code via signal line 8.

The process of recording information on the record carrier is controlled by the control circuit 94, which for this purpose is loaded with a suitable control program. Such a control program may comprise, for example the steps indicated in the flow chart in Fig. 9.

Step S1 is performed immediately after the record carrier 1 has been loaded into the recording device. In this step S1 the read/write head 82 is set to the read mode and by means of the signals V3 and V4 on the output of the decoder circuit 96 it is ascertained whether the code word supplied to the output of the converter 92 comprises an auxiliary code containing the address values AVO or AVI. When the presence of these auxiliary code is detected the values of AVO and AVI are stored in a memory of the control circuit.

Subsequently, in step S2, the read/write head 82 is directed towards the track portion containing the address code of the value AVI with the aid of the address code being read, and by means of the table of contents recorded in this track portion the address in the program area is determined before recording of the next data signal can commence. The manner in which said start address can be specified in such a

table of contents is described in detail in Netherlands Patent Application NL-A-8900766 (PHN 12.887). Said Patent Application describes how a provisional table of contents can be recorded in the lead-in area. As soon as all the desired data signals have been recorded a definitive table of contents is recorded in the lead-in area in a manner as prescribed by the CD standard.

In step S3 the read/write head 82 is directed towards the track portion having the start address specified in the provisional table of contents by means of the address codes being read. When this track portion is reached the read/write head 82 is set to the write mode, after which the data signal Vi applied to the EFM modulation circuit 100 can be recorded. For the absolute time codes to be inserted into the subcode Q-channel of the recorded signal the counts of the counting circuit 97 may be used, which modes are read by the control circuit 94 via a bus 102 and which are subsequently applied to the EFM modulation circuit 100 via the bus 101. Moreover, the value of each count being read is compared with the value AVO and if the count is larger than or equal to the value AVO recording of the data signal is discontinued. Recording is also terminated once the complete data signal to be recorded has been recorded. Upon termination of the recording process step S4 is performed, in which it is ascertained whether recording has been discontinued as a result of the track portion specified by the value AVO being reached. If this is the case, step S5 is performed, in which the lead-out signal is recorded during a predetermined time interval, the subcode information characterizing the lead-out signal being applied to the EFM modulation circuit by the control circuit 94. After recording of the lead-out signal the write/read head 82 is directed towards the lead-in area in step S6 to record the definitive table of contents in the lead-in area.

If during step S4 it is ascertained that recording of the data signal has not been discontinued as a result of the track portion having the address code with the value AVO being reached, the provisional table of contents is recorded in the lead-in area during step 57. Subsequently, it is checked in step S8 whether further data signals are to be recorded on the record carrier. If this is the case the program is terminated. If this is not the case, the definitive table of contents is recorded in the lead-in area in step S9 and the lead out signal is recorded in step S10, after which the program is terminated.

In the foregoing the invention has been illustrated for an optical recording system for recording a standard CD signal on a record carrier comprising substantially concentric tracks. However, it is to be noted that the invention equally applies to the recording of signals in linear tracks. Moreover, the invention may also be applied to the recording of other data signals than CD signals. Neither is the scope of the invention limited to optical recording systems. It may equally be applied to magneto-optical recording systems or magnetic recording systems where address codes have been recorded in the track by means of a previously applied track modulation.

Claims

1. An information-recording system, comprising a record carrier of an inscribable type, having a preformed track in which an auxiliary signal comprising address codes is recorded by means of a preformed track modulation, which address codes specify the addresses of the track portions in which said codes are recorded, and a recording device for recording information in the track provided with the track modulation, which recording device comprises read means for reading the auxiliary signal, characterized in that in addition to address codes the auxiliary signal also comprises auxiliary codes which can be distinguished from the address codes, the recording device comprising means for selectively extracting the address codes and the auxiliary codes from the auxiliary signal, the auxiliary codes comprisng control data for the purpose of controlling the recording process, the recording device being adapted to control the recording process in dependence upon the extracted auxiliary codes.

2. An information recording system as claimed in Claim 1, characterized in that the control data specifies a desired write energy setting for recording the information, the recording device comprising means for adjusting the write energy, the recording device being further adapted to adjust the write energy in dependence upon the extracted auxiliary codes.

3. An information recording system as claimed in Claim 1 or 2, characterized in that the address codes and the auxiliary codes have the same data format.

4. An information-recording system as claimed in Claim 1 to 3, characterized in that each time one of the address codes contained in the auxiliary signal or one of the address codes contained in the auxiliary signal is recorded at equidistant locations in the track, the address codes each time indicating the distance, measured in the track direction, between the location where they have been recorded and a specific reference position, the recording device comprising synchronising means for deriving a clock signal from the auxiliary signal being read, which clock signal is in synchronism with the process of reading the

equidistantly recorded addres and auxiliary codes, a counter controlled by the clock signal, and means responsive to a correctly read address code to set the count to a value corresponding to the address code being read.

5. An information-recording system as claimed in Claim 4, characterized in that the address codes comprise absolute time codes specifying said distance as a playing time.

6. An information-recording system as claimed in any one of the Claims 3, 4 or 5, characterized in that the auxiliary code can be distinguished from the address codes by specific bit combinations which do not occur in the address codes.

7. An information-recording system as claimed in any one of the Claims 1 to 6, characterized in that the track modulation comprises a periodic track excursion in a direction transverse to the track direction, the frequency of said periodic track excursion being modulated in conformity with the auxiliary signal, the read means comprising detection means for detecting the track modulation and for generating a detection signal of a frequency corresponding to the frequency of the detected track modulation, and a frequency demodulator for detecting frequency variations in the detection signal for the purpose of recovering the auxiliary signal.

8. An information-recording system as claimed in any one of the Claims 2 to 7, characterized in that the track comprises track portions which are substantialy concentric with a centre of rotation, the control data indicating the address code of at least a track portion having a predetermined radial position relative to said centre.

9. An information-recording system as claimed in Claim 8, characterized in that the recording device comprises means for recording contents information depending on the auxiliary codes being read, which information indicates the addresses of recorded portions of the information signal, in a track portion having a predetermined radial position specified by the auxiliary codes.

10. An information-recording system as claimed in Claim 9, characterized in that the predetermined radial position is situated at the periphery of the track pattern formed by the concentric track portions.

11. An information-recording system as claimed in any one of the Claims 8 to 10, characterized in that the recording device comprises means for recording a lead out signal indicating the end of the recorded information signal, and means for determining, depending on the auxiliary codes being read, the track portion where the recording of the lead-out signal should commence in such a way that the beginning of the recording of the lead-out signal is situated before a position indicated by auxiliary codes.

12. An information-recording system as claimed in any one of the Claims 8, 9 or 10, characterized in that the reference position is situated in a track portion having a predetermined radial position.

13. A recording device for use in an information-recording system as claimed in any one of the preceding Claims.

14. A record carrier for use in an information-recording system as claimed in any one of the Claims 1 to 13.

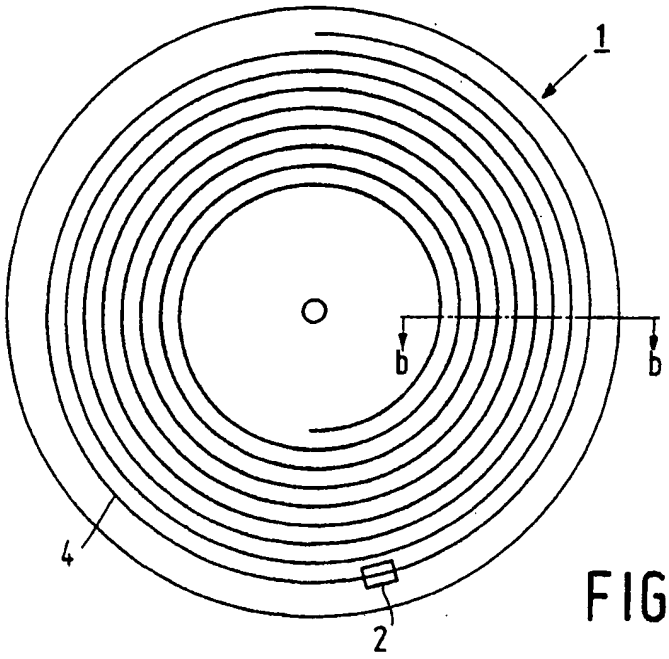

FIG.1a

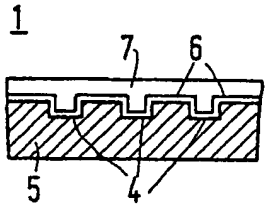

FIG.1b

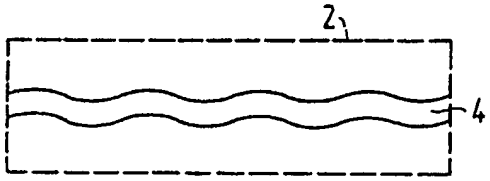

FIG.1c

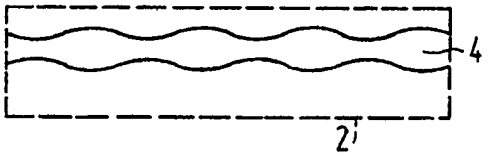

FIG.1d

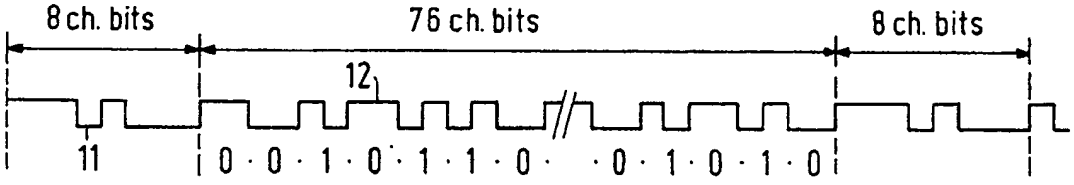

FIG.2

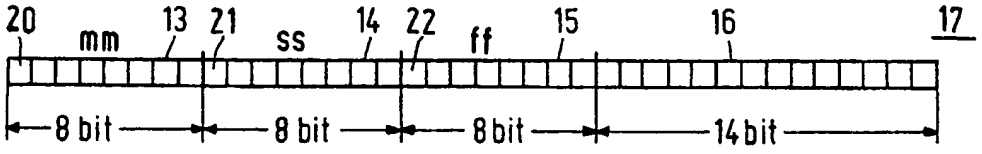

FIG.3

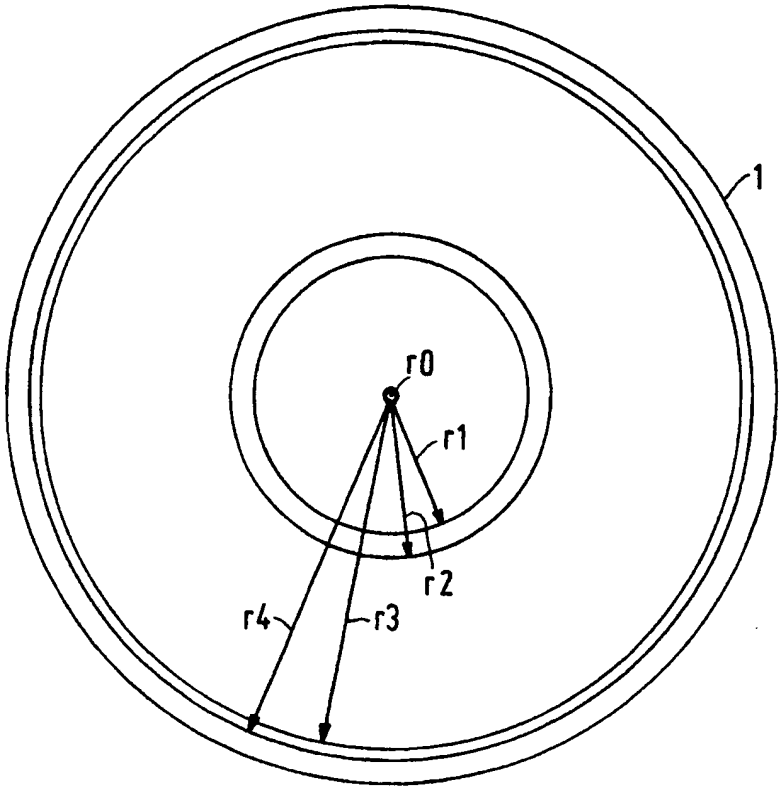

FIG.4

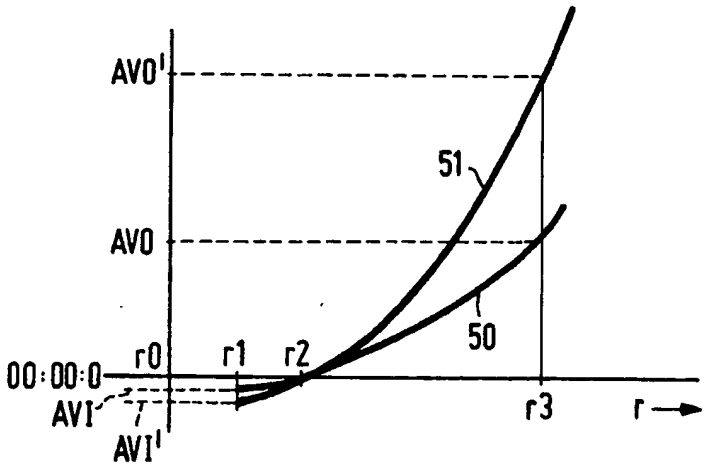

FIG.5

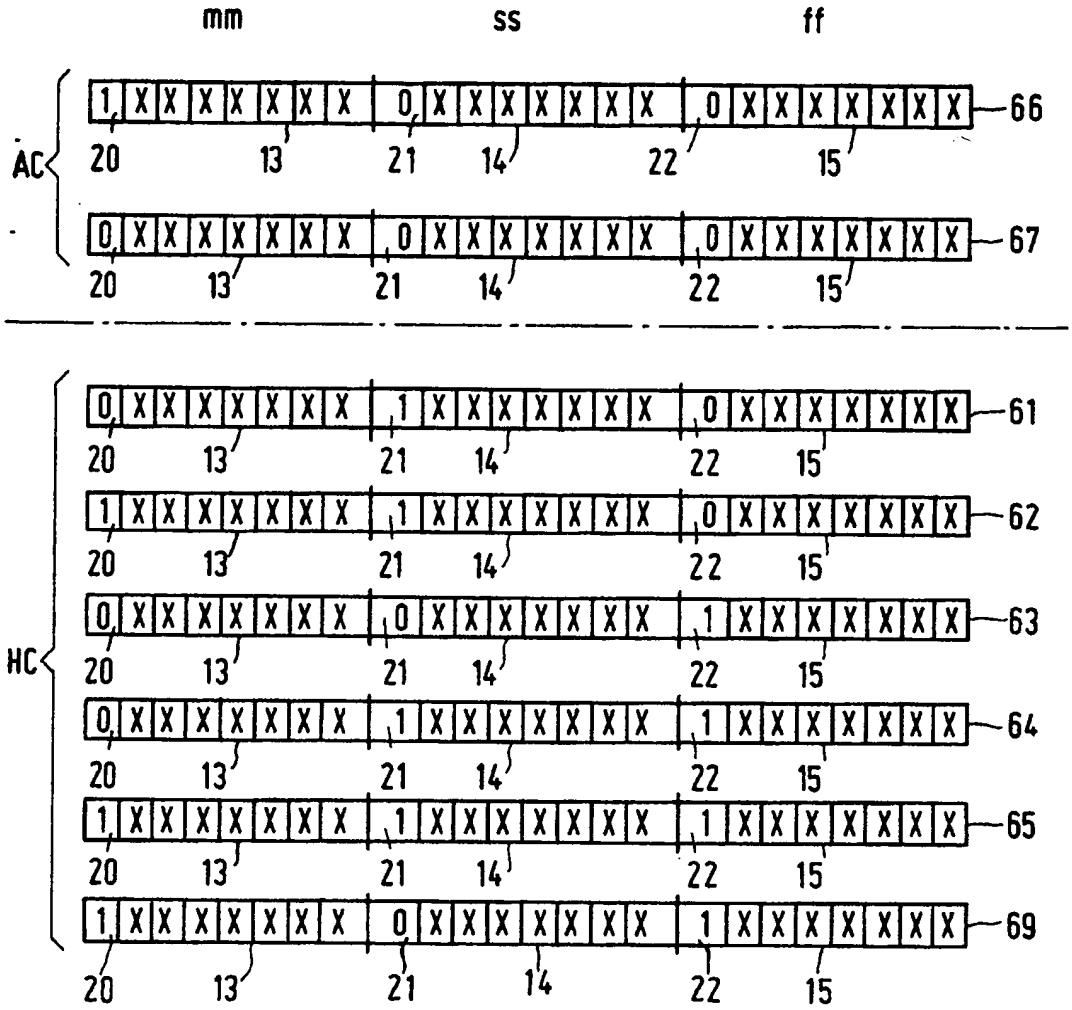
mm
ss
ff
AC
HC
66
67
61
62
63
64
65
69
20
13
21
14
22
15

FIG.6

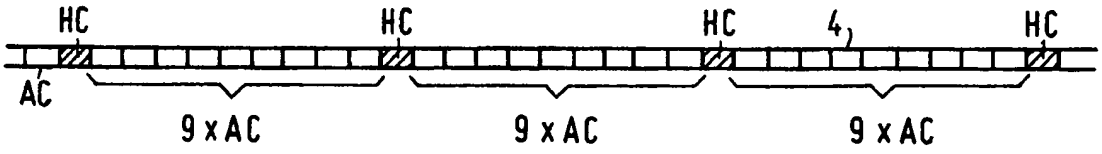
HC
AC
4
9 x AC
9 x AC
9 x AC

FIG.7

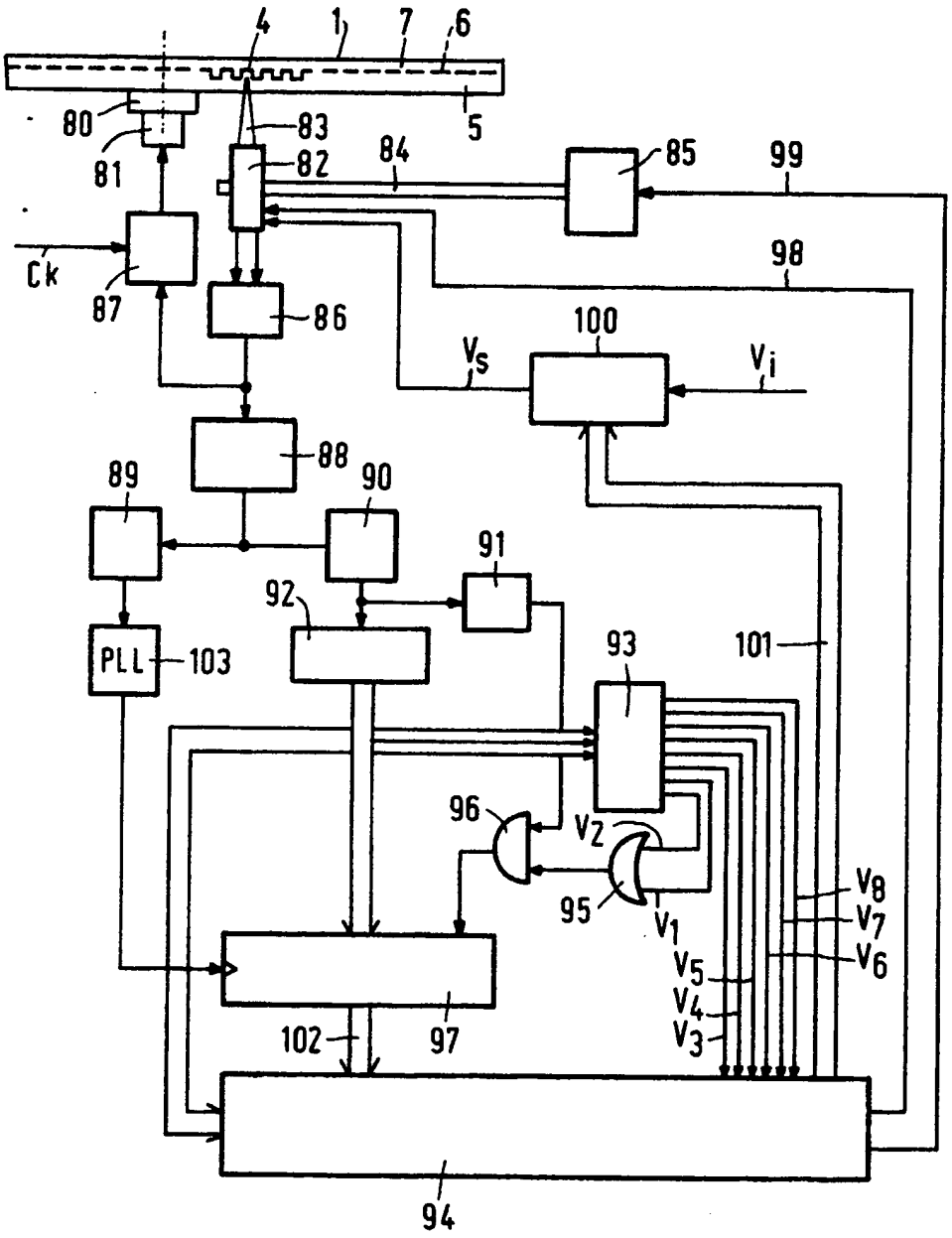
4
1
7
6
5
80
81
83
82
84
85
99
98
Ck
87
86
100
Vs
Vi
88
89
90
91
92
93
101
PLL
103
96
V2
95
V1
V8
V7
V6
V5
V4
V3
102
97
94

FIG.8

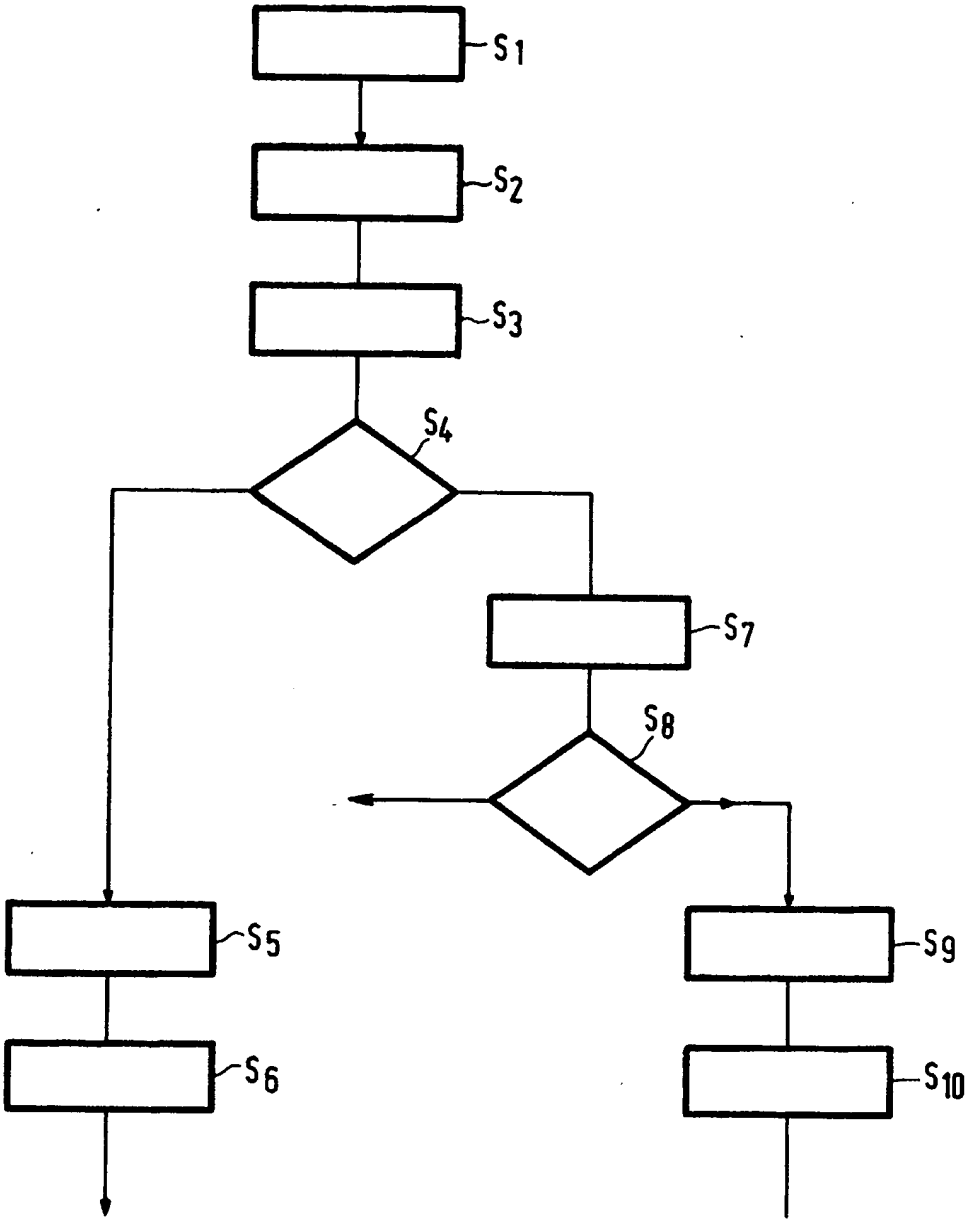

FIG.9

European Patent Office

EUROPEAN SEARCH REPORT

Application Number

EP 90 20 1094

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	EP-A-0288114 (PHILIPS AND DUPONT OPTICAL COMPANY) * the whole document *	1, 2	G11B27/30 G11B7/013 G11B11/10 G11B27/36
A	* the whole document *	5, 8, 13, 14	
D	&NL,8700934*not published*		
X	PATENT ABSTRACTS OF JAPAN vol. 11, no. 93 (P-559)(2540) 24 March 1987, & JP-A-61 243974 (SANYO ELECTRIC CO.,LTD.) 30 October 1986, * the whole document *	1, 2	
A		13, 14	
A	EP-A-0265849 (HITACHI,LTD.) * the whole document *	1, 2, 13, 14	
A	JP,A,1014731*PIONEER ELECTRONIC CORP.*18-01-1989 &PATENT ABSTRACTS OF JAPAN, Vol.13,no.188*P-866* *3536* May 8,1989, *see Abstract of JP,A,1014731*	1, 2, 13, 14	
A	PATENT ABSTRACTS OF JAPAN vol. 12, no. 288 (P-741)(3135) 08 August 1988, & JP-A-63 064632 (NEC CORP.) 23 March 1988, * the whole document *	1, 2, 13, 14	TECHNICAL FIELDS SEARCHED (Int. Cl.5) G11B
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A	EP-A-0164131 (VICTOR COMPANY OF JAPAN,LIMITED) * the whole document *	1, 3, 5, 8-14	
A	EP-A-0292917 (SONY CORPORATION) * the whole document *	1, 5, 8-14	

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The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	31 JULY 1990	DAALMANS F.J.

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A	EP-A-0265695 (SONY CORPORATION) * the whole document *	1, 5, 7, 13, 14	
A D,A	EP-A-0265984 (N.V.PHILIPS'GLOEILAMPENFABRIEKEN) * the whole document * &NL,A,8700655	1, 5, 7, 13, 14	
A D,A	EP-A-0299573 (N.V.PHILIPS'GLOEILAMPENFABRIEKEN) * the whole document * &NL,A,8701632	1, 5, 7, 13, 14	
A	GB-A-2145555 (BRITISH BROADCASTING CORP ET AL.) * the whole document *	1, 13, 14	
A	US-A-4682317 (TOMISAWA) * the whole document *	1, 4	
A	DE-A-3615468 (ROBERT BOSCH GMBH.) * the whole document *	1, 4	
P,X D	EP-A-0342748 (N.V.PHILIPS'GLOEILAMPENFABRIEKEN) * the whole document * &NL,8801275*not published*	1, 5-7	TECHNICAL FIELDS SEARCHED (Int. Cl.5)
P,X P,A	EP-A-0326215 (N.V.PHILIPS'GLOEILAMPENFABRIEKEN) * the whole document * * the whole document *	1, 2, 5 6, 13, 14	
P,A D	EP-A-0325330 (N.V.PHILIPS'GLOEILAMPENFABRIEKEN) * the whole document * &NL,A,8800152	1, 4, 5, 7, 13, 14	

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
THE HAGUE	31 JULY 1990	DAALMANS F.J.

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